**Product Line Architecture**

**Speakers:**

**Jon Wade**  
Professor and Associate Dean of Research  
Stevens Institute of Technology, USA

Jon Wade is a Professor in the School of Systems and Enterprises at the Stevens Institute of Technology and currently serves as the Associate Dean of Research where he is leading research in the areas of complex systems, complexity management and the use of technology in systems engineering and STEM education. Previously, Dr. Wade was the Executive Vice President of Engineering at International Game Technology where he managed corporate wide research and development. Before joining IGT, Dr. Wade managed the development of the UltraSPARC V based Enterprise Server family at Sun Microsystems. Prior to this, he led supercomputer development at Thinking Machines Corporation. Dr. Wade received his SB, SM, EE and PhD degrees in Electrical Engineering and Computer Science from the Massachusetts Institute of Technology.